

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

VIJAY S. MENON, ET AL.

Application No.: 10/658,897

Filed: September 10, 2003

For: **A Method and Apparatus for Hardware
Data Speculation to Support Memory
Optimizations**



Art Group: 2189

Examiner: Flournoy, Horace L.

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted before the mailing of a final Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

03/17/2006 SDENB081 00000030 10658897

01 FC:1806

180.00 OP

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

The fee set in the amount of \$180.00 for submission of the Information Disclosure Statement is enclosed herewith. Please charge any additional fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

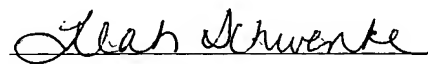
Date: March 15, 2006



Ashley R. Ott, Reg. No. 55,515

12400 Wilshire Boulevard, 7th Floor
Los Angeles, CA 90025
Telephone: (303) 740-1980

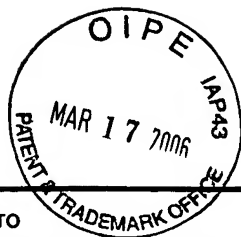
I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



Leah Schwenke

03-15-06

Date



Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT****Complete if Known**

Application Number	10/658,897
Filing Date	September 10, 2003
First Named Inventor	Vijay S. Menon
Art Unit	2189
Examiner Name	Flournoy, Horace L.
Attorney Docket Number	42P17027

Sheet 2 of 2

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		W. Pugh, The Java Memory Model is Fatally Flawed. Concurrency: Practice and Experience, vol. 12, no. 6, pp. 445-455, 2000.	
		M. Ahamad, et al. The Power of Processor Consistency. In Proceedings of the Fifth ACM Symposium on Parallel Algorithms and Architectures (SPAA), 1993.	
		D. Scales. Impact of "reads kill" in Java. http://www.cs.umd.edu/~pugh/java/readsKillImpact.html , May 1999.	
		Gontmakher and A. Schuster. Java Consistency: Non-operational Characterizations for the Java Memory Behavior. Technical Report CS0922, Dept of C.S., Technion, Nov. 1997.	
		Intel Itanium Architecture Software Developer's Manual, Vol. 1: Application Architecture, Revision 2.0, pp. 1:58-1:62, December, 2001.	
		R.R. Goodman. Cache Consistency and Sequential Consistency. Technical Report 61, IEEE Scalable Coherent Interface Working Group, March 1989.	

Examiner
SignatureDate
Considered

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

¹Applicant's unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.
Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450